

JTAG III – a cheap and simple JTAG interface for XILINX CPLD and FPGA programming

General

The JTAG III is a simple JTAG programming interface compatible with the XILINX parallel cable 3. Because of the compatibility it can be used with the free-of-charge available XILINX WebPack ISE CPLD and FPGA programming tools.

In conjunction with this software the device allows you download data to XILINX FPGAs and CPLDs.

The device is connected to a PC parallel port.



Features

- JTAG port fully decoupled from PC parallel port
- Supply voltage range 2.5 to 5V
- JTAG port with 10 pin dual row pinheader, optionally adaptable to single row 9 pin
- JTAG port pin to pin compatible with our EB-XC95 XILINX CPLD evaluation board
- power supply comes from target circuit, no additional power supply necessary
- Cost optimized design

Package content

- Basic device JTAG III
- User documentation
- 10 pin ribbon cable
- Adaptor 10 pin dual row to 9 pin single row

Order Number

Order number "JTAG III": Standard setup

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